

Notice of References Cited	Application/Control No. 09/901,806	Applicant(s)/Patent Under Reexamination SLAVIN ET AL.	
	Examiner Satish S. Rampuria	Art Unit 2124	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,014,723 A	01-2000	Tremblay et al.	711/1
	B	US-6,240,499 B1	05-2001	Spencer, Matthew Stephen	711/170
	C	US-5,586,325 A	12-1996	MacDonald et al.	717/153
	D	US-6,279,152 B1	08-2001	Aoki et al.	717/150
	E	US-2002/0123981 A1	09-2002	Baba et al.	707/1
	F	US-6,721,760 B1	04-2004	Ono et al.	707/104.1
	G	US-6,282,583 B1	08-2001	Pincus et al.	709/400
	H	US-5,537,359 A	07-1996	Toda, Haruki	365/222
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Verdoer et al., Access and Alignment of Arrays for a Bidimensional Parallel Memory, 1994, IEEE, Pages 364-356.
	V	Weinhardt et al., Memory access optimisation for reconfigurable systems, May 2001, IEEE, Pages 105-112.
	W	Sarawagi et al., Efficient Organization of Large Multidimensional Arrays, 1994, IEEE, Pages 328-336.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.